

TIMING SIGNAL GENERATING CIRCUIT AND RECEIVER CIRCUIT
DESIGNED TO SPEED UP SIGNAL TRANSMISSION

5

ABSTRACT OF THE DISCLOSURE

A timing signal generating circuit receives a
10 plurality of input signals of differing phases and
generates a timing signal having a phase intermediate
therebetween. The timing signal generating circuit has a
plurality of current polarity switching circuits, and a
voltage level correction circuit. Each of the current
15 polarity switching circuits is provided between a
plurality of current sources and acts to switch an output
current polarity in accordance with a corresponding one
of the input signals. The voltage level correction
circuit corrects the voltage level of a phase-combined
20 signal produced by combining weighted outputs of the
plurality of current polarity switching circuits.